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Code design of l_{t_i/n_i} -ispotty-byte error correcting θ -codes

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Abstract. l_{t_i/n_i} -ispotty-byte error correcting codes over \mathbf{Z}_q [7] are non-binary codes generalizing the binary irregular-spotty-byte codes [3] suitable for semiconductor memories in which a word is divided into irregular bytes not necessarily of the same length. The distance between distinct words is measured in terms of newly defined l_{t_i/n_i} -metric function [7]. In this paper, we present the code construction methods of l_{t_i/n_i} -ispotty-byte error correcting codes in terms of their parity check matrix.

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